

What is claimed is:

1. A circuit comprising:

at least one first power amplifier;

at least one first matching circuit coupled to the at least one first power amplifier; and,

at least one first filter coupled to the at least one first matching circuit,

wherein a signal received by the at least one first power amplifier is transmitted to the at least one first filter through the at least one first matching circuit.

2. The circuit of claim 1, further comprising a decoder coupled to the at least one first power amplifier.

3. The circuit of claim 1, further comprising at least one switch coupled to the at least one first filter.

4. The circuit of claim 1, further comprising:

at least one second power amplifier;

at least one second matching circuit coupled to the at least one second power amplifier; and,

at least one second filter coupled to the at least one second matching circuit,

wherein a signal received by the at least one second power amplifier is transmitted to the at least one second filter through the at least one second matching circuit.

5. The circuit of claim 4, further comprising a decoder coupled to the at least one first and at least one second power amplifiers.

6. The circuit of claim 5, further comprising at least one switch coupled to the at least one first and at least one second power amplifiers, and to the at least one first and at least one second filters.

7. The circuit of claim 1, wherein the at least one first power amplifier, the at least one first matching network, and the at least one first filter are all disposed on a leadframe.

8. The circuit of claim 7, wherein the leadframe includes forty-two connector pads.

9. The circuit of claim 6, wherein the at least one switch comprises a single pole six throw switch.

10. The circuit of claim 6, wherein the at least one switch is coupled to at least one antenna terminal.

11. The circuit of claim 6, wherein the at least one switch is coupled to at least one reception terminal.

12. The circuit of claim 5, wherein the decoder provides control signals to the at least one first and at least one second power amplifiers.

13. The circuit of claim 12, wherein the decoder provides control signals to at least one switch coupled to the at least one first and at least one second filters.

14. The circuit of claim 1, wherein the at least one first filter comprises a low pass filter.

15. The circuit of claim 4, wherein the at least one first and at least one second filters both comprise low pass filters.

16. The circuit of claim 4, wherein the at least one first and second power amplifiers, the at least one first and second matching networks, and the at least one first and second filters are all disposed on a leadframe.

17. A method for transmitting a signal, comprising the steps of:

- applying a first signal to a first input port of a circuit module;
- amplifying and filtering the first signal in the circuit module; and,
- providing the amplified and filtered signal at a first output port of the circuit module.

18. The method of claim 17, comprising the further steps of:

- applying a second signal to a second input port of a circuit module;
- amplifying and filtering the second signal in the circuit module; and,
- providing the amplified and filtered signal at a second output port of the circuit

module.

19. A telecommunications system comprising:

an antenna; and,

a front end module coupled to the antenna,

wherein the front end module includes at least one first power amplifier, at least one first matching circuit coupled to the at least one first power amplifier, and at least one first filter coupled to the at least one first matching circuit, wherein a signal received by the at least one first power amplifier is transmitted to the at least one first filter through the at least one first matching circuit.